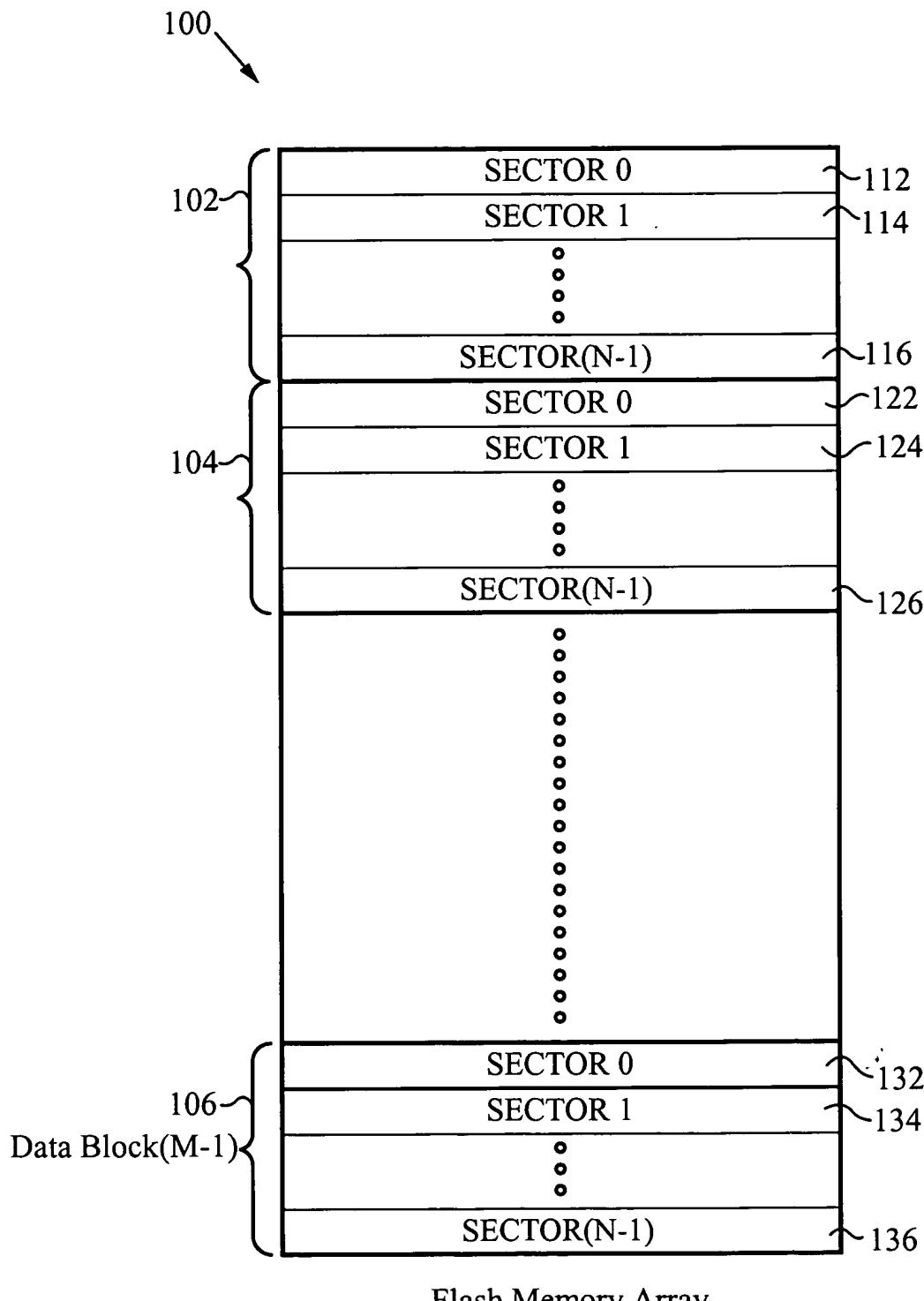


1/21



Flash Memory Array

Fig. 1 (PRIOR ART)

OIC to enter
8/10/04
OT



2/21

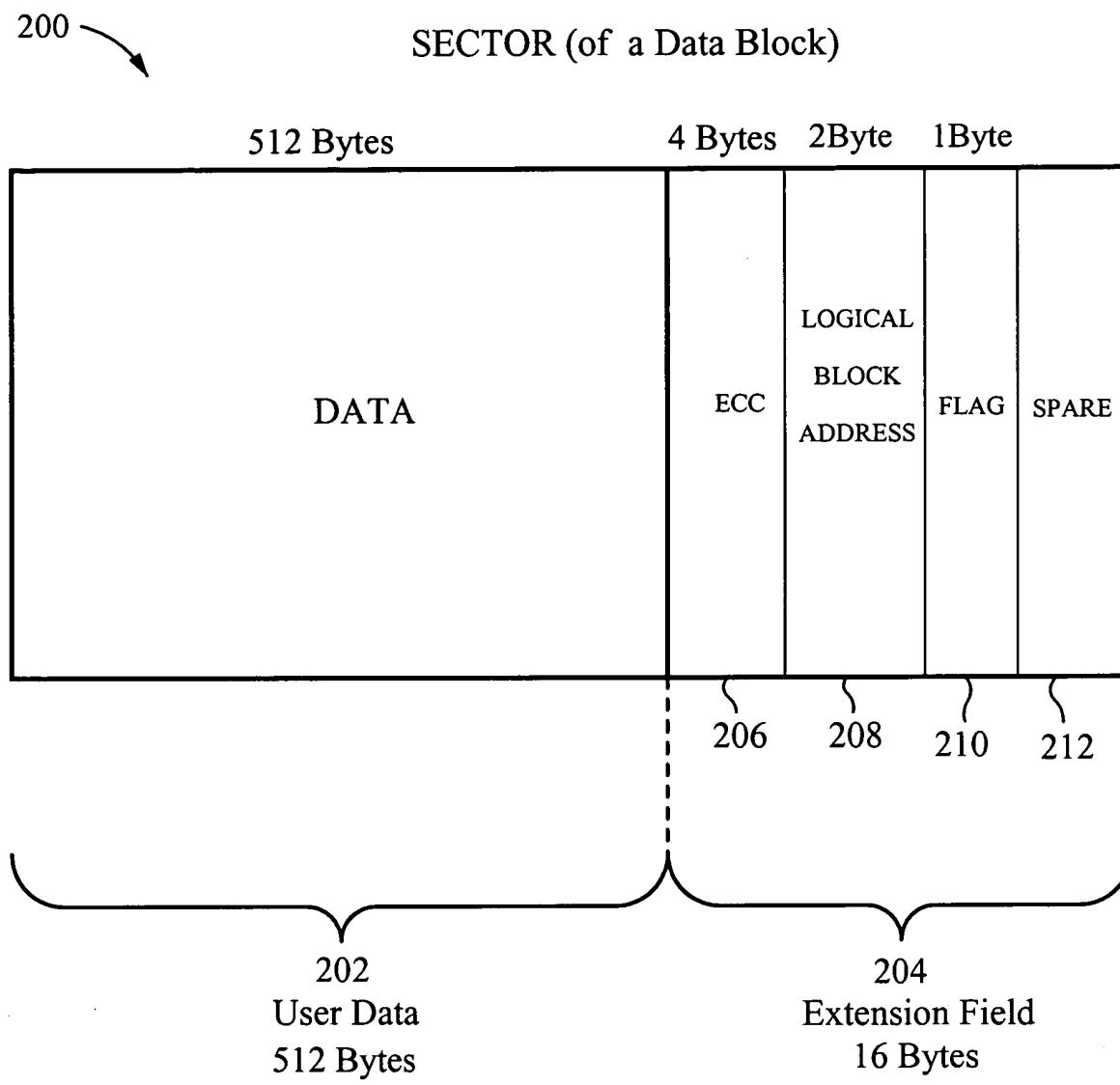


Fig. 2 (PRIOR ART)



5/21

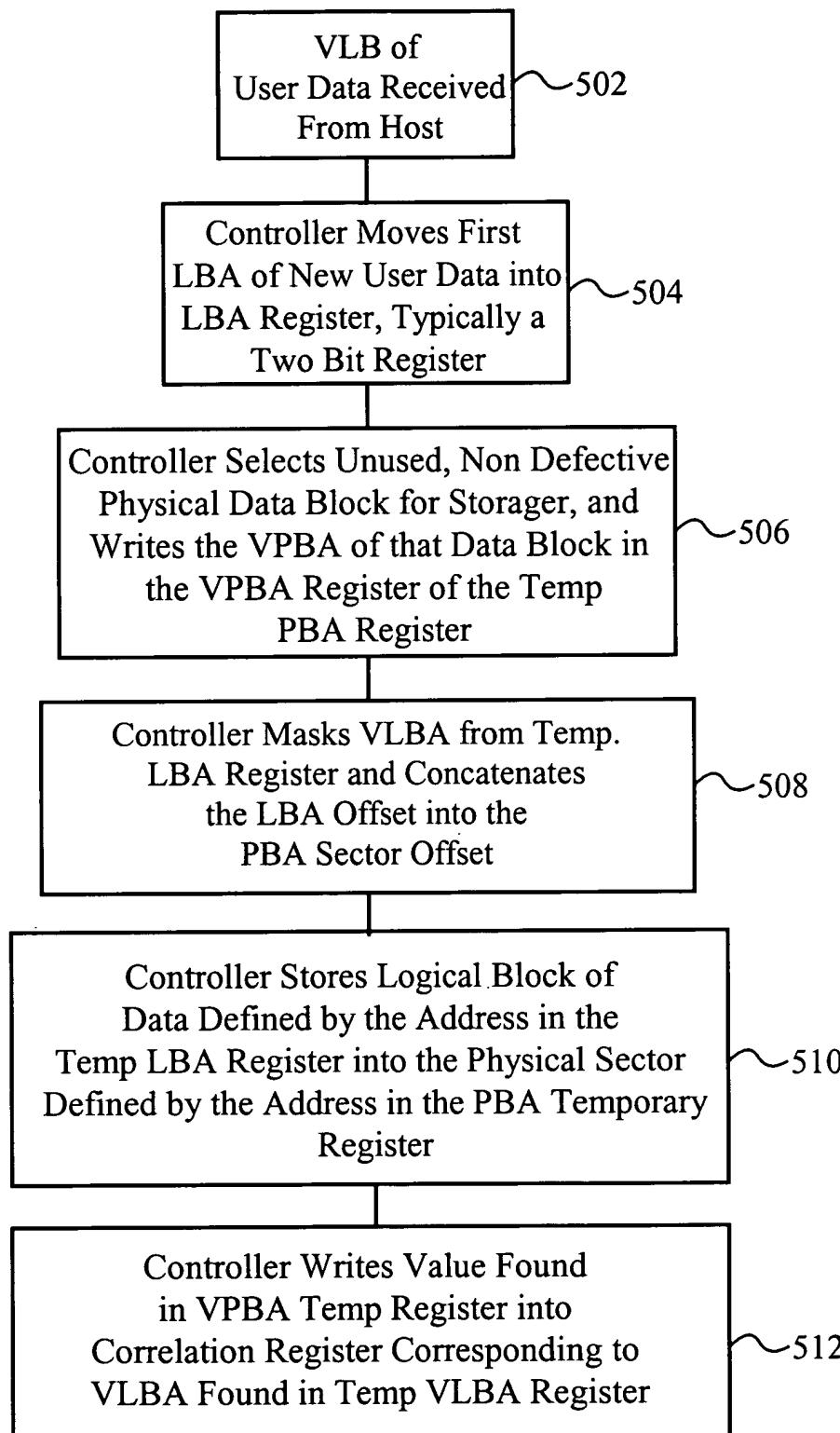


Fig. 5 (PRIOR ART)



6/21

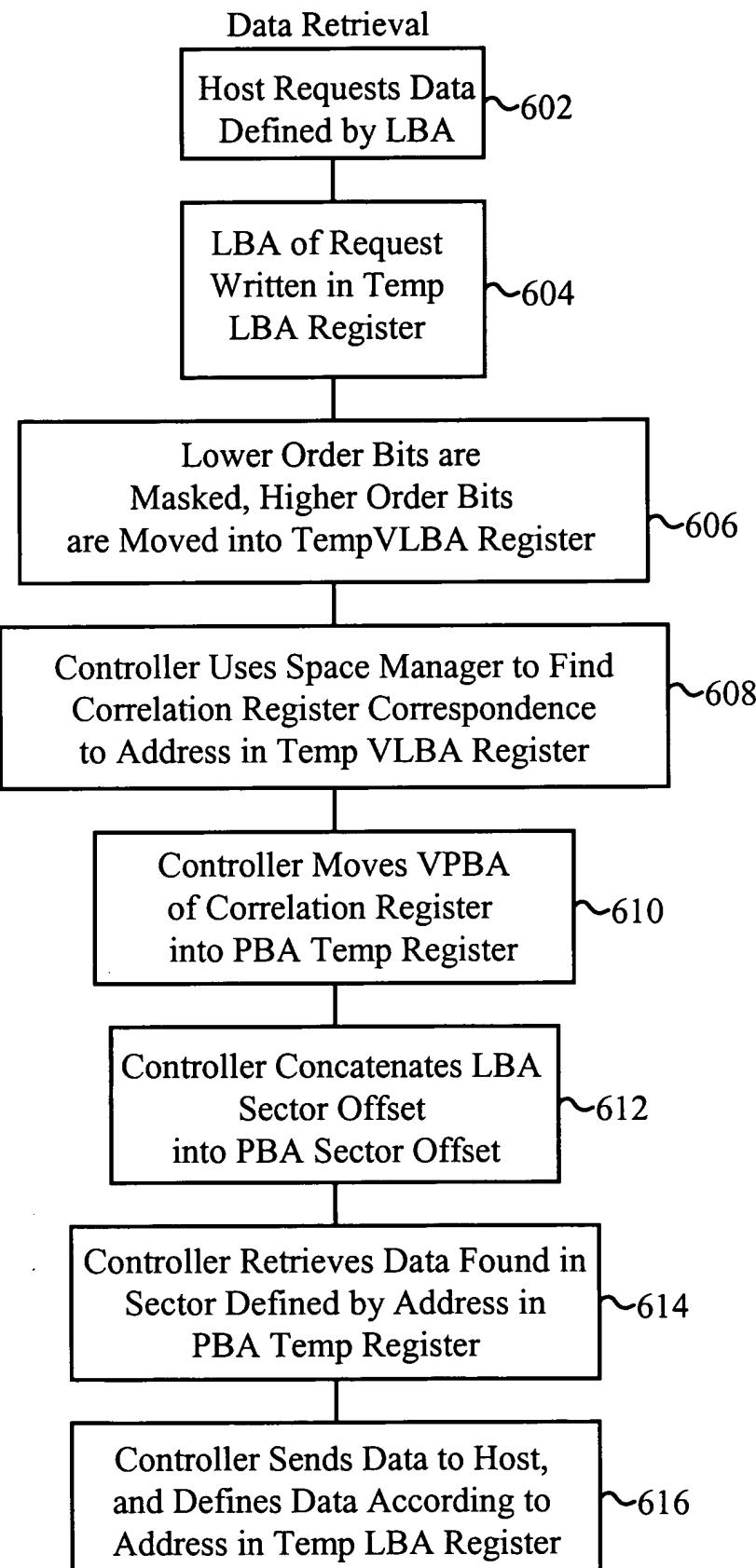


Fig. 6 (PRIOR ART)